



DOCKET NO.: H0498.70154US00

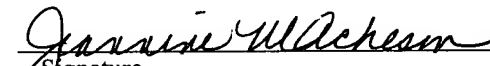
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Charles M. Lieber et al.  
Serial No.: 09/935,776  
Confirmation No.: 8935  
Filed: August 22, 2001  
For: DOPED ELONGATED SEMICONDUCTORS, GROWING SUCH  
SEMICONDUCTORS; DEVICES INCLUDING SUCH  
SEMICONDUCTORS AND FABRICATING SUCH DEVICES  
Examiner: Shouxiang Hu  
Art Unit: 2811

---

**CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)**

The undersigned hereby certifies that this document is being placed in the United States mail with first-class postage attached, addressed to MAIL STOP RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the 28<sup>th</sup> day of February, 2006.

  
Signature

---

**Mail Stop RCE**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**STATEMENT FILED PURSUANT TO THE DUTY OF  
DISCLOSURE UNDER 37 C.F.R. §§1.56, 1.97 AND 1.98**

Sir:

Pursuant to the duty of disclosure under 37 C.F.R. §§1.56, 1.97 and 1.98, the Applicants request consideration of this Information Disclosure Statement.

**PART I: Compliance with 37 C.F.R. §1.97**

This Information Disclosure Statement has been filed before the mailing of a first Office action after the filing of a request for continued examination under 37 C.F.R. §1.114.

No fee or certification is required.

PART II: Information Cited

The Applicants hereby make of record in the above-identified application the information listed on the attached form PTO-1449 (modified PTO/SB/08). The order of presentation of the references should not be construed as an indication of the importance of the references.

The Applicants hereby make the following additional information of record in the above-identified application.

The Applicants would like to bring to the Examiner's attention the following co-pending applications that may contain subject matter related to this application:

<u>Serial No.</u>	<u>Filing Date</u>	<u>Inventor(s)</u>
11/082,372	03-17-2005	Lieber et al.
11/012,549	12-15-2004	Lieber et al.
11/172,408	06-30-2005	Lieber et al.

PART III: Remarks

Documents cited anywhere in the Information Disclosure Statement are enclosed unless otherwise indicated. It is respectfully requested that:

1. The Examiner consider completely the cited information, along with any other information, in reaching a determination concerning the patentability of the present claims;
2. The enclosed form PTO-1449 (modified PTO/SB/08) be signed by the Examiner to evidence that the cited information has been fully considered by the Patent and Trademark Office during the examination of this application;
3. The citations for the information be printed on any patent which issues from this application.

By submitting this Information Disclosure Statement, the Applicants make no representation that a search has been performed, of the extent of any search performed, or that more relevant information does not exist.

Serial No.: 09/935,776  
Conf. No.: 8935

- 3 -

Art Unit: 2811

By submitting this Information Disclosure Statement, the Applicants make no representation that the information cited in the Statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

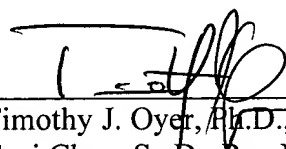
By submitting this Information Disclosure Statement, the Applicants make no representation that the information cited in the Statement is, or is considered to be, in fact, prior art as defined by 35 U.S.C. §102.

Notwithstanding any statements by the Applicants, the Examiner is urged to form his or her own conclusion regarding the relevance of the cited information.

An early and favorable action is hereby requested.

Respectfully submitted,

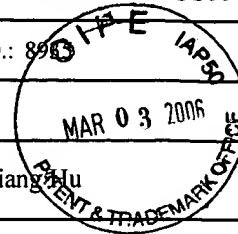
By:

  
\_\_\_\_\_  
Timothy J. Oyer, Ph.D., Reg. No. 36,628  
Tani Chen, Sc.D., Reg No. 52,728  
WOLF, GREENFIELD & SACKS, P.C.  
600 Atlantic Avenue  
Boston, Massachusetts 02210-2206  
Telephone: (617) 646-8000

Date: February 28, 2006

xNDDx

<b>FORM PTO-1449/A and B (modified PTO/SB/08)</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				APPLICATION NO.: 09/935,776		ATTY. DOCKET NO.: H0498.70154US00	
				FILING DATE: August 22, 2001		CONFIRMATION NO.: 896	
				APPLICANT: Lieber et al.			
				GROUP ART UNIT: 2811		EXAMINER: Shouxiang Hu	
Sheet	1	of	1				



#### U.S. PATENT DOCUMENTS

Examiner's Initials #	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or Issue of Cited Document MM-DD-YYYY
		Number	Kind Code		
		6,503,375	B1	Maydan et al.	01-07-2003
		6,872,645	B2	Duan et al.	03-29-2005
		2002/0013031	A1	Chen et al.	01-31-2002
		2002/0130311	A1	Lieber et al.	09-19-2002
		2005/0110064	A1	Duan et al.	05-26-2005
		2005/0161662	A1	Majumdar et al.	07-28-2005
		2005/0201149	A1	Duan et al.	09-15-2005
		2005/0202615	A1	Duan et al.	09-15-2005
		2005/0212079	A1	Stumbo et al.	09-29-2005
		2005/0214967	A1	Scher et al.	09-29-2005
		2005/0219788	A1	Chow et al.	10-06-2005
		2005/0230356	A1	Empedocles et al.	10-20-2005

#### FOREIGN PATENT DOCUMENTS

Examiner's Initials #	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Translation (Y/N)
		Office/Country	Number	Kind Code			
		WO	04/003535	A1	Nanosys, Inc.	01-08-2004	
		WO	05/089165	A2	Nanosys, Inc.	09-29-2005	
		WO	05/094440	A2	Nanosys, Inc.	10-13-2005	

#### OTHER ART — NON PATENT LITERATURE DOCUMENTS

Examiner's Initials #	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)
		Jin et al., "Scalable Interconnection and Integration of Nanowire Devices without Registration," <i>NanoLetters</i> , 2004, 4(5):915-919.	
		Lieber, "Nanoscale Science and Technology: Building a Big Future from Small Things," <i>MRS Bulletin</i> , July 2003, pp. 486-491.	
		Lu et al., "One-dimensional hole gas in germanium/silicon nanowire heterostructures," 2005, 102(29):10046-10051.	
		Zhong et al., "Nanowire Crossbar Arrays as Address Decoders for Integrated Nanosystems," <i>Science</i> , 2003, 302:1377-1379.	
		Office Action dated 08-30-2005 in U.S.S.N. 10/020,004	
		Office Action dated 05-25-2005 in U.S.S.N. 10/196,337	
		International Preliminary Examination Report in PCT/US01/48230, dated 5/20/2003	
		PCT International Search Report for PCT/US03/22061, dated 06/22/04, International Filing Date 07/16/2003	

EXAMINER:	DATE CONSIDERED:
-----------	------------------

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.  
 \*a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No. \_\_, filed \_\_, and relied upon for an earlier filing date under 35 U.S.C. 120 (continuation, continuation-in-part, and divisional applications).  
 [NOTE - No copies of U.S. patents, published U.S. patent applications, or pending, unpublished patent applications stored in the USPTO's Image File Wrapper (IFW) system, are included. See 37 CFR §1.98 and 1287OG163. Copies of all other patent(s), publication(s), unpublished, pending U.S. patent applications, or other information listed are provided as required by 37 CFR §1.98 unless 1) such copies were provided in an IDS in an earlier application that complies with 37 CFR §1.98, and 2) the earlier application is relied upon for an earlier filing date under 35 U.S.C. §120.]